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Docket No.: M4065.0100/P100-B  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Howard E. Rhodes

Application No.: 10/617,706

Confirmation No.: 3719

Filed: July 14, 2003

Art Unit: 2814

For: CMOS IMAGER WITH SELECTIVELY  
SILICIDED GATES

Examiner: T. N. Quach

**STATUS INQUIRY**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive a Reissued Certificate of Correction from the Patent and Trademark Office in response to our Request dated January 27, 2006.

Dated: December 15, 2006

Respectfully submitted,

By

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